

IT1 - Test Plan Checklist

CLARITY

1. Does the Test Plan clearly specify the order of the steps of all integration testing?

COMPLETENESS

1. Does the Test Plan specify the overall approach and policy for acceptance test?
2. Does the Test Plan include a description of the type of hardware and software system environment to be used?
3. Does the Test Plan define success criteria for all tests?
4. Does the Test Plan adequately describe the functions being tested?
5. Does the Test Plan explicitly describe those functions that will not be tested during integration test?
6. Does the Test Plan describe conditions under which testing will be halted and resumed during integration test?
7. Does the test case set adequately exercise all significant code changes, particularly interface modifications?
8. Does the Test Plan adequately describe integration test baselines?
9. For a phased delivery, does the Test Plan establish test baselines in each phase for use in the next phase?
10. Does the Test Plan define sufficient and proper regression testing?

COMPLIANCE

1. Does the Test Plan list all the specifications, standards, and documents necessary for its development?

CONSISTENCY

1. Has the order of integration tests been defined to match the order of integration specified in higher-level documents?
2. Is the Test Plan consistent with higher-level test plan documents?

CORRECTNESS

1. Are the Test Plan entrance and exit criteria realistic?
2. Are all necessary drivers and stubs identified and available to test the function as specified?
3. Are all dependencies between the input simulator and the hardware addressed?

DATA USAGE

1. Does the test case set include adequate coverage of illegal and conflicting input combinations?
2. Does the test case set include adequate usage of default input values?
3. Does the test case set exercise an adequate number of program error paths?

FUNCTIONALITY

1. Is the Test Plan adequate to meet acceptance criteria?

INTERFACES

1. Does the test case set adequately exercise the handling of information flow across external interfaces?

LEVEL OF DETAIL

1. Is the coverage of the test case set sufficiently complete to provide confidence that the functions being tested operate correctly within their intended environment?

MAINTAINABILITY

1. Are control and incorporation of changes to the specifications, design, or coding that may occur during test contained in the Test Plan?

PERFORMANCE

1. Are performance goals for the test procedures explicitly stated?

RELIABILITY

1. Is sufficient test data collected and documented to support estimation of the software's reliability?

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TESTABILITY

1. Is the testing approach feasible?
2. Are all those requirements considered untestable and unable to be tested identified, and is it explained why they are untestable or unable to be tested?
3. Has development and procurement of test facilities (input simulators and output analyzers), methods, and tools been scheduled with adequate lead time?
4. Are the testing schedules described to a sufficient level of detail (testing schedules are described for each individual function to be tested)?
5. Is the method of estimating resource usage required for testing identified?
6. For multiple builds, have all requirements been identified on a per-build basis?
7. Have the roles and responsibilities for all personnel involved in the test activity been identified?
8. Is the specification of test facilities consistent with the test success criteria?
9. Are there any scheduling conflicts among the testing personnel schedules?
10. Does the Test Plan call for the participation of independent quality assurance personnel to verify test activity?
11. Does the Test Plan call for independent testing?

TRACEABILITY

1. Do the acceptance tests exercise each requirement specified in higher level documents (FRD, FDD, SRD)?
2. Are the test acceptance criteria traceable to higher level requirements documents (SIS, UG/SOM, FRD, SRD, FDD)?
3. Does the test case set for integration test exercise each interface described in higher level documents (SIS and SSD)?